






12/19/00

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

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APPLICATION NO. 09/742171	CONT/PRIOR	CLASS 716	SUBCLASS 7	ART UNIT 2825	EXAMINER <i>HAMPSON</i>
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. H. ...</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. H. ...</b>
3. <b>Secretary</b>	3. <b>Mr. J. H. ...</b>
4. <b>Treasurer</b>	4. <b>Mr. J. H. ...</b>
5. <b>Member</b>	5. <b>Mr. J. H. ...</b>
6. <b>Member</b>	6. <b>Mr. J. H. ...</b>
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8. <b>Member</b>	8. <b>Mr. J. H. ...</b>
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93. <b>Member</b>	93. <b>Mr. J. H. ...</b>
94. <b>Member</b>	94. <b>Mr. J. H. ...</b>
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Steven Teig  
Joseph Ganley

Method and apparatus for quantifying the quality of placement configurations in a partitioned region of an integrated-circuit layout

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<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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